3.3 Processor Architecture

A pipeline. Pipelining is the cheaper form of parallelism because expensive hardware, like a floating point multiplier, is needed only once, and therefore, pipelining is found in most general purpose processors. The regular flow of instructions through the pipeline, however, comes to a halt, if the execution of an operation needs the result of a previous operation that is still in the pipeline and has not yet progressed to a stage where this result is already available. The instruction (and all that follow) is then delayed until the required operand is computed. This situation is called a “pipeline stall”. It is best avoided by scheduling a mix of unrelated instructions for execution.

Producing fast code is a very hard problem. It requires to find a balance between contradicting requirements that depend very much on the target processor and its facilities for caching and pipelining. This task is best left to the compiler that generates the machine code and different compilers achieve different results.

We compare the GNU C compiler version 2.95.3[12], the Intel C Compiler version 7.0 [16], and the Optimizing Microsoft 32-Bit C/C++ compiler version 12.00.8804 [23], as well as four different implementations: The DCT used in mpg123 version pre0.59s[22], the DCT used in mad version 0.14.2b[21], the DCT derived with spiral (not used), and the CosDFT derived with spiral actually used with mp32pcm. In Table 10, the run times are given in $\mu$s. We postpone a complete discussion of performance until section 3.6.

<table>
<thead>
<tr>
<th>C Compiler</th>
<th>mad</th>
<th>mpg123</th>
<th>DCT</th>
<th>CosDFT</th>
</tr>
</thead>
<tbody>
<tr>
<td>GNU</td>
<td>0.84</td>
<td>1.47</td>
<td>1.09</td>
<td>1.13</td>
</tr>
<tr>
<td>Intel</td>
<td>0.62</td>
<td>0.43</td>
<td>0.31</td>
<td>0.31</td>
</tr>
<tr>
<td>Microsoft</td>
<td>0.71</td>
<td>0.79</td>
<td>0.56</td>
<td>0.49</td>
</tr>
</tbody>
</table>

Tab. 10: Run times in $\mu$s for different implementations and compilers

3.4 Eliminating the Shifting

The vector $v$ contains 16 blocks of 32 subband samples each. The shifting is needed because the windowing step uses each of these blocks successively in all 16 positions within $v$ (Fig. 11). If we do not want to move the data, we can instead move the start point of $v$ inside a larger vector $w$ (Fig. 12). Of course, we can not move the start point indefinitely since the amount of memory available for $w$ is limited. When we reach the left end of $w$ we have to start over at the right end and at this point, we have to make a copy of $v$, which is now at the left end, because next it will be needed at the right end of $w$ (Fig. 13).

Increasing the storage by $n$ blocks, therefore implies that we can skip the copying $n$ times before copying is necessary again. Any value of $n > 0$ is possible. With just one block more, we can cut the amount of copying in half. Another good choice could be 16 additional blocks—doubling the amount of memory. This reduces the time spent with copying quite drastically (to 1/16). For very different reasons—we want to keep a certain amount of “historic” data from the stream—as explained in section A.1, we choose to have $n = 2 \times 18 + 15 = 51$. 
(private declarations 1) ≡
(1)
#define WINDOWBLOCKS 16
#define SHIFTBLOCKS (2 * BLOCKS + WINDOWBLOCKS - 1)
#define SHIFTSIZE (SHIFTBLOCKS * SUBBANDS)
#define CHANNELS 2

Used in 6, 7, and 130.

(stream data 2) ≡
(2)
double w[CHANNELS][SHIFTSIZE]; Used in 42.

Now instead of copying v each time, we need to copy it only every 36th time. The shifting requires moving the beginning of v inside w. For this purpose, we store as part of the (stream data 2) for each channel an offset that indicates the intended start of v inside of w.

(stream data 2) +≡
(3)
int offset[CHANNELS] ;
In each shifting step, the intended beginning of $v$ moves to the left to make space for new subband samples. If the offset becomes negative, we have to copy $16 - 1$ blocks and start over 16 blocks left of the right end of $w$. This is the C code to

\[ s\rightarrow{\text{offset}}[ch] = s\rightarrow{\text{offset}}[ch] - \text{SUBBANDS}; \]
\[ \text{if } (s\rightarrow{\text{offset}}[ch] < 0) \]
\[ \{ s\rightarrow{\text{offset}}[ch] = \text{SHIFTSIZE} - \text{WINDOWBLOCKS} \times \text{SUBBANDS}; \]
\[ \text{memmove}((s\rightarrow{\text{w}}[ch][s\rightarrow{\text{offset}}[ch] + \text{SUBBANDS}]), (s\rightarrow{\text{w}}[ch][0]), \]
\[ \text{sizeof(double)} \times (\text{WINDOWBLOCKS} - 1) \times \text{SUBBANDS}); \]
\[ v = s\rightarrow{\text{w}}[ch] + s\rightarrow{\text{offset}}[ch]; \]

After that, $v$ is shifted and ready to receive the next block of data.

3.5 The Core Algorithm

What remains, after the elimination of shifting, as the core of the algorithm is captured by the functions \textit{dct32} and \textit{windowing}.

\[ \text{extern void dct32(const double } y, \text{double } v); \]
\[ \text{extern void windowing(const double } v, \text{mp3_sample } x); \]

\textit{dct32} takes a vector $y$ of scaled samples performs the 32 point CosDFT on $y$, with the output ending up in vector $v$. The \textit{windowing} function takes $v$ and writes output samples to vector $x$.

The code for the 32 point CosDFT can be found in appendix A.2 and the code to (apply windowing) is in appendix A.3.

Since the performance of the decoder depends largely on these two routines They are written to a separate file, \textit{perform.c}, to facilitate further optimization.

\[ \text{#include "mp32pcm.h"} \]
\[ \text{private declarations } \]
\[ \text{conversion from double to mp3_sample } \]
\[ \text{void windowing(const double } v, \text{mp3_sample } x) \]
\[ \{ \text{apply windowing } \}
\[ \text{void dct32(const double } y, \text{double } v) \]
\[ \{ \text{32 point CosDFT } \}

3.6 Performance

After all these optimizations, we return to the question of performance. The distribution of the runtime of mp32pcm is shown in Tab. 14. It is now dominated by the windowing step, which inevitably requires about 500 additions and multiplications, while the CosDFT needs only a reasonable fraction of the overall time. The reading of bit is only critical inside the main loop, where we read the subband samples. Decoding the header or reading the bit allocation and scalefactor information contributes
almost nothing to the overall runtime. Still, the reading of the subband samples is surprisingly expensive, since apart from some bit manipulation and table lookup, only a single multiplication is required per sample.

<table>
<thead>
<tr>
<th>Activity</th>
<th>Intel[16]</th>
<th>GNU[12]</th>
<th>References</th>
</tr>
</thead>
<tbody>
<tr>
<td>read bit allocation</td>
<td>1 %</td>
<td>1 %</td>
<td>see section 7.4</td>
</tr>
<tr>
<td>read scalefactors</td>
<td>1 %</td>
<td>1 %</td>
<td>see section 7.9</td>
</tr>
<tr>
<td>read subband samples</td>
<td>15 %</td>
<td>25 %</td>
<td>see section 7.6</td>
</tr>
<tr>
<td>CosDFT</td>
<td>25 %</td>
<td>30 %</td>
<td>see appendix A.2</td>
</tr>
<tr>
<td>windowing</td>
<td>56 %</td>
<td>41 %</td>
<td>see appendix A.3</td>
</tr>
<tr>
<td>rest</td>
<td>2 %</td>
<td>2 %</td>
<td></td>
</tr>
</tbody>
</table>

*Tab. 14: Distribution of runtime for different compilers*

In Tab. 15, the times are given in ms for running the different decoders on the layer I file *f11.mpg*, provided with Part 4 of the standard[2].

<table>
<thead>
<tr>
<th>Compiler</th>
<th>mad[21]</th>
<th>mpg123[22]</th>
<th>mp32pcm 32 bit</th>
<th>mp32pcm 16 bit</th>
</tr>
</thead>
<tbody>
<tr>
<td>GNU[12]</td>
<td>5.83 ms</td>
<td>3.29 ms</td>
<td>3.11 ms</td>
<td>3.55 ms</td>
</tr>
<tr>
<td>Intel[16]</td>
<td>5.27 ms</td>
<td>3.23 ms</td>
<td>2.30 ms</td>
<td>2.70 ms</td>
</tr>
<tr>
<td>Microsoft[23]</td>
<td>6.02 ms</td>
<td>4.49 ms</td>
<td>4.39 ms</td>
<td>4.29 ms</td>
</tr>
</tbody>
</table>

*Tab. 15: Run times in ms for different implementations and compilers*

Some remarks may help to interpret this table:

- **mad** uses fixed point binary fractions with operations that can be implemented through integer arithmetic and shifting. Different formats of these numbers can be used to adjust the speed accuracy tradeoff. But even if optimized for speed, the decoder runs slower than **mpg123** or **mp32pcm**. Due to the extensive use of hand-optimized code, the differences between various compilers are relatively small.

- **mpg123** is commonly regarded as the fastest decoder available. Again there are various options to influence the speed/quality tradeoff. The influence of the compiler on the speed is minor because critical parts (the DFT) are written in assembly language.

- **mp32pcm** is slightly faster than **mpg123** when compiled with the Intel compiler and slower when compiled for 16 bit with the GNU compiler. The difference is due to the faster code that the Intel compiler produces for CosDFT and windowing. For this reason, the distribution of **mp32pcm** supplies the assembly output of the Intel compiler for these parts (in file *perform.s*) as an alternative to the C sources (in *perform.c*). If using the file *perform.s*, the GNU compiler will produce a decoder as fast as the Intel compiler.